



SPECIAL CALL FOR VISVESVARAYA PhD SCHEME UNDER C2S PROJECT

Applications are invited from Indian Citizens for the VISVESVARAYA PhD Scheme for project entitled “**Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications**” under Chip-to-Startup (C2S) programme (Category –II) funded by the Ministry of Electronics & Information Technology (MeitY), Govt. of India. The details are as follows:

1.	Name of the Scheme	VISVESVARAYA PhD Scheme
2.	Duration of the Project	5 years
3.	No. of positions	2
4.	Essential Qualifications	<p>i. First class in B.E/B.Tech. - Electronics and Communication Engineering / Electronics and Instrumentation Engineering / Electrical and Electronics Engineering / Computer Science and Engineering / Information Technology with minimum of 60% of marks or 6.5 CGPA from any recognized university / Institute.</p> <p>AND</p> <p>ii. First class in M.E/M.Tech. - VLSI/ Microelectronics/ Signal Processing/ Communication systems / SoC-Design/ Applied Electronics / Power Electronics / Embedded systems/ IoT/ Computer Science and Engineering / Information Technology with minimum of 60% of marks or 6.5 CGPA from any recognized university / Institute.</p>
5.	Emoluments	<p>Visvesvaraya PhD Scheme: Rs. 38,750/- per month (I & II year) Rs. 43,750/- per month (III to V year – on satisfactory progress)</p>
6.	Date & Time of Interview	<i>Will be intimated later through email</i>
7.	Venue	<i>Room No: 318, Sir C.V. Raman Academic and Administrative Block, IIIT Tiruchirappalli, Sethurapatti, Tiruchirappalli - 620012</i>
8.	Selection Process	Written test followed by interview.
9.	Last date for Applying	19.01.2026

Note:

1. Eligible candidates are required to attend the selection process **with all original academic and experience certificates** (if any) and a **valid photo identity proof**.

2. **Further, bring one set of photo copy of the testimonials mentioned below.**

Details of the Documents/Testimonials	Online Submission Procedure
a. Detailed CV/Resume* b. SSLC Marksheets* c. HSLC Marksheets (or) Diploma Consolidated Marksheets & Degree* d. B.E/B.Tech Consolidated Marksheets & Degree* e. M.E/M.Tech Consolidated Marksheets & Degree* f. UG/PG Transfer Certificate* g. GATE Score (if any) h. Work Experience Certificates (if any) i. Research Experience Certificates (if any) j. Community Certificate (if applicable) k. Valid Government ID proof*	Kindly fill in the google forms link given below: https://forms.gle/vxjtz9NdixT3gtHY6

***Mandatory**

3. Candidates before appearing for the selection process shall ensure that they are eligible for the position they intend to apply.
4. The shortlisted candidates will be intimated about the selection process through e-mail.
5. No TA/DA is admissible for attending the selection process.
6. The decision of the selection committee is final.
7. Canvassing in any form leads to the rejection of the candidature.
8. Selected candidates will not be permitted to claim any regular/part-time appointments in this institute or any other during this period.
9. The Institute reserves the right not to fill up the posts, cancel the advertisement in whole or in part, without assigning any reason and the decision of the Director of IIIT Tiruchirappalli, in this regard shall be final.

C2S Project Team